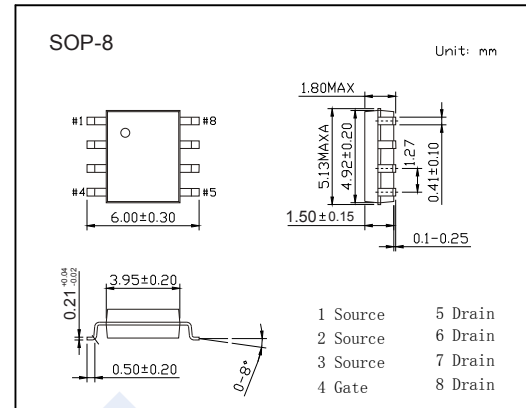
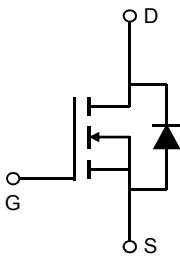


N-Channel MOSFET

AO4450 (KO4450)

■ Features

- $V_{DS} (V) = 40V$
- $I_D = 7 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 30m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 38m\Omega (V_{GS} = 4.5V)$



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V_{DS}	40	V	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current	I_D	$T_A=25^\circ C$	7	A
		$T_A=70^\circ C$	5.5	
Pulsed Drain Current	I_{DM}	45		
Avalanche Current	I_{AS}	14		
Avalanche Energy	$L=0.1mH$	EAS	10	mJ
Power Dissipation	PD	$T_A=25^\circ C$	3.1	W
		$T_A=70^\circ C$	2	
Thermal Resistance.Junction- to-Ambient	R_{thJA}	$t \leq 10s$	40	$^\circ C/W$
		Steady-State	75	
Thermal Resistance.Junction- to-Lead	R_{thJL}	24		
Junction Temperature	T_J	150	$^\circ C$	
Storage Temperature Range	T_{stg}	-55 to 150		

N-Channel MOSFET

AO4450 (KO4450)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	V _{DSS}	I _D =250μA, V _{GS} =0V	40			V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V			1	μA	
		V _{DS} =40V, V _{GS} =0V, T _J =55°C			5		
Gate-Body Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.7		3	V	
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =7A			30	mΩ	
		V _{GS} =10V, I _D =7A, T _J =125°C			45		
		V _{GS} =4.5V, I _D =5A			38		
On State Drain Current	I _{D(ON)}	V _{GS} =10V, V _{DS} =5V	45			A	
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =7A		30		S	
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =20V, f=1MHz		516		pF	
Output Capacitance	C _{oss}			82			
Reverse Transfer Capacitance	C _{rss}			43			
Gate Resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz		4.6		Ω	
Total Gate Charge (10V)	Q _g	V _{GS} =10V, V _{DS} =20V, I _D =7A		8.9	13	nC	
Total Gate Charge (4.5V)				4.3	7		
Gate Source Charge			Q _{gs}		2.4		
Gate Drain Charge			Q _{gd}		1.4		
Turn-On DelayTime	t _{d(on)}	V _{GS} =10V, V _{DS} =20V, R _L =2.8Ω, R _{GEN} =3Ω		6.4		ns	
Turn-On Rise Time	t _r			3.6			
Turn-Off DelayTime	t _{d(off)}			16.2			
Turn-Off Fall Time	t _f			6.6			
Body Diode Reverse Recovery Time	t _{rr}	I _F =7A, di/dt=100A/μs		18		nC	
Body Diode Reverse Recovery Charge	Q _{rr}			10			
Maximum Body-Diode Continuous Current	I _S				3.5	A	
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V			1	V	

Note : The static characteristics in Figures 1 to 6 are obtained using <300us pulses, duty cycle 0.5% max.

■ Marking

Marking	4450 KC****
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N-Channel MOSFET AO4450 (KO4450)

■ Typical Characteristics

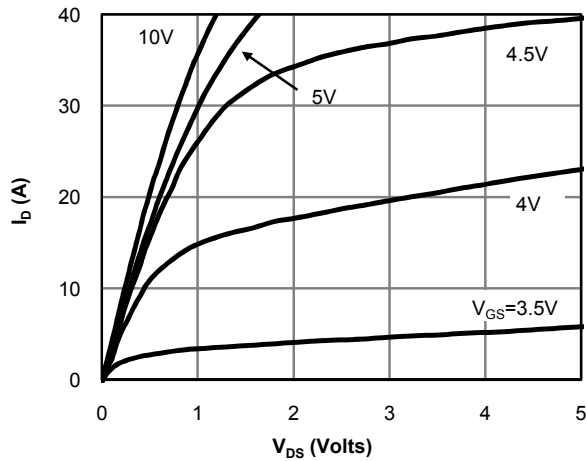


Fig 1: On-Region Characteristics (Note E)

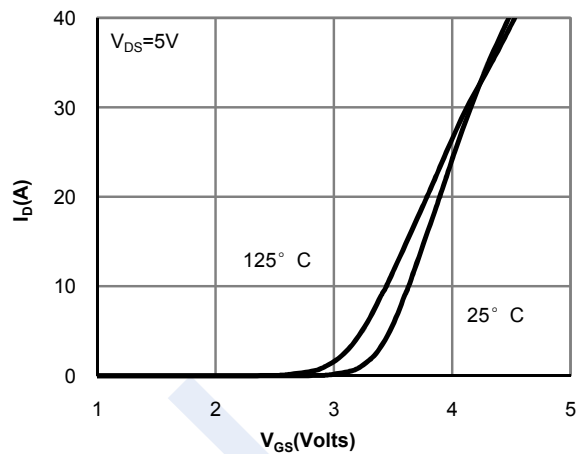


Figure 2: Transfer Characteristics (Note E)

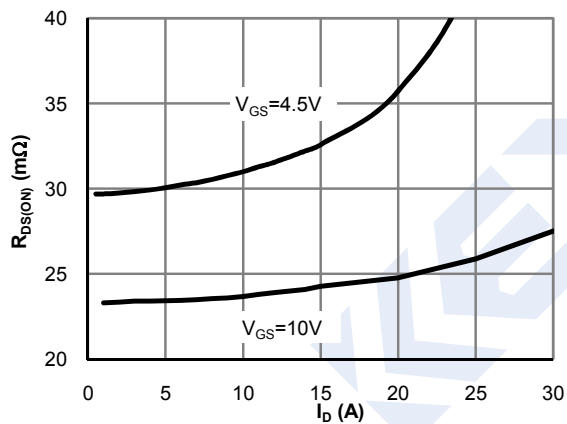


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

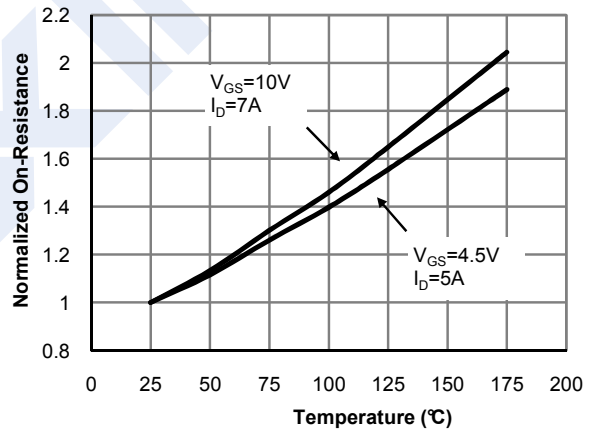


Figure 4: On-Resistance vs. Junction Temperature (Note E)

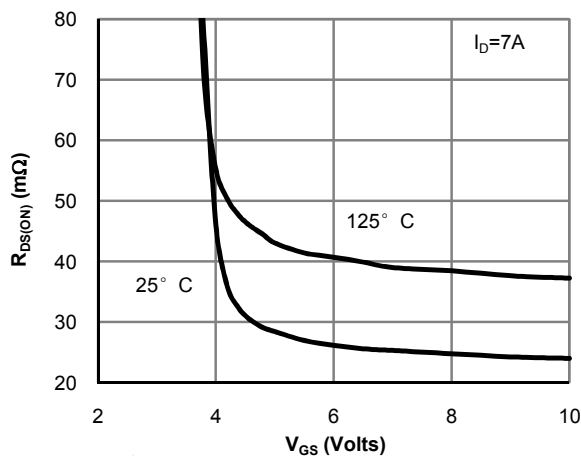


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

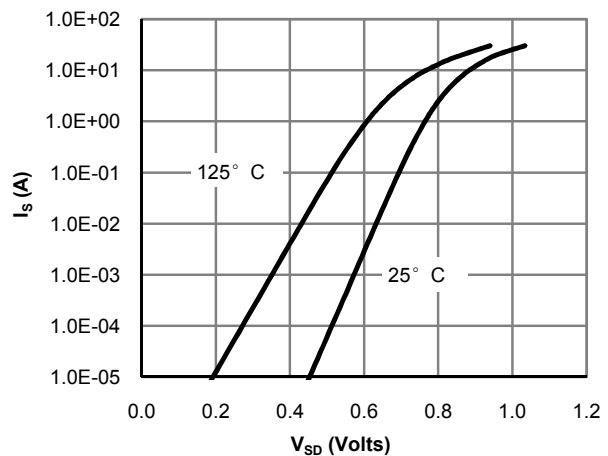


Figure 6: Body-Diode Characteristics (Note E)

N-Channel MOSFET AO4450 (KO4450)

■ Typical Characteristics

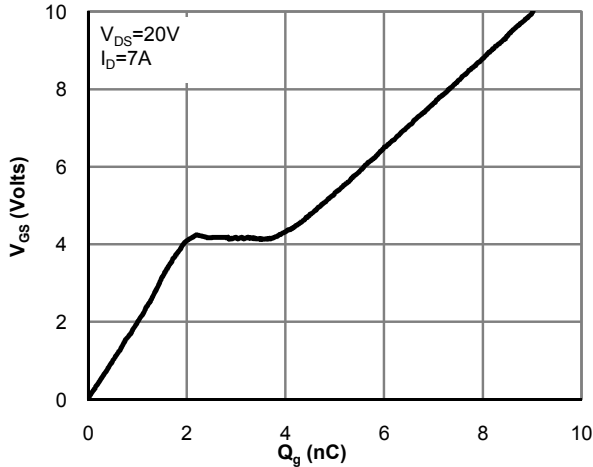


Figure 7: Gate-Charge Characteristics

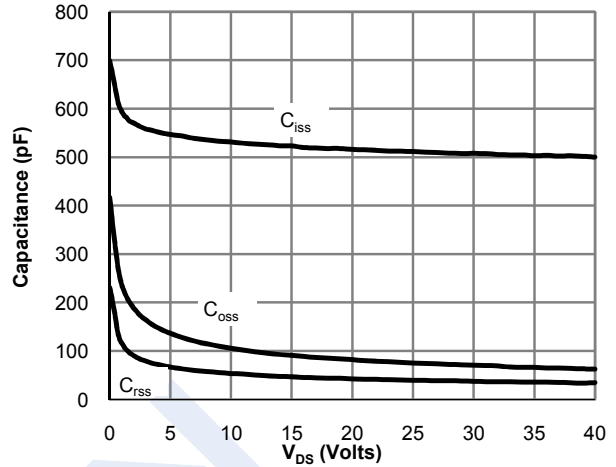


Figure 8: Capacitance Characteristics

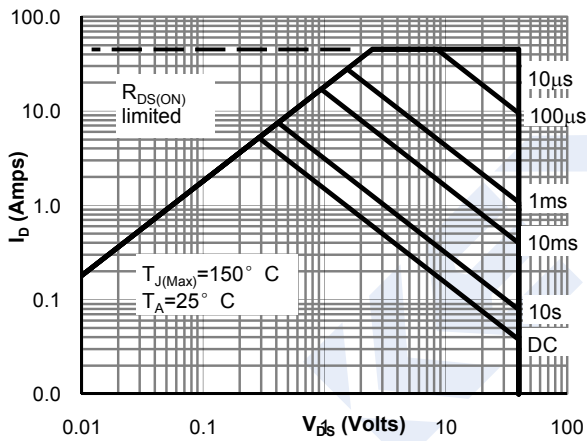


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

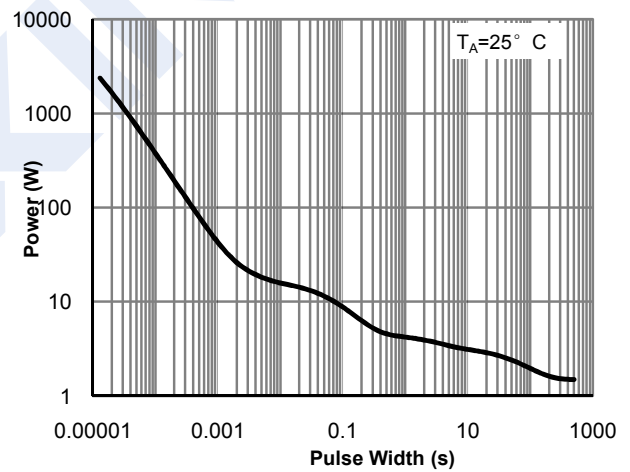


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

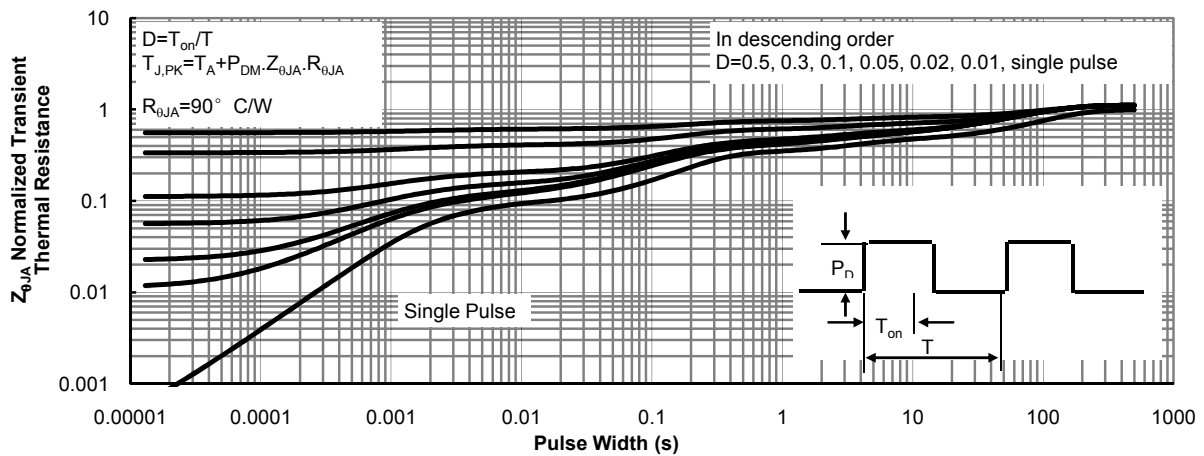


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)